PTO-1449 (Modified)	ATTY. DOCKET NO. RA043D2C6C2	SERIAL NUMBER 09/801,151
U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	APPLICANT(S) FARMWALD ET AL.	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT	FILING DATE March 7, 2001	GROUP ART UNIT 2818

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EXAMINER INITIALS	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE
$T N T$	5,287,532	Feb. 15, 1994	Hunt	712	10	
	5,157,776	Oct. 20, 1992	Foster	711	131	
	5,023,838	Jun. 11, 1991	Herbert	365	189.08	
	4,961,171	Oct. 2, 1990	Pinkham et al.	365	230.05	
	4,930,065	May 22, 1990	McLagan et al.	710	50	
	4,641,276	Feb. 3, 1987	Dunki-Jacobs	710	71	
	4,639,890	Jan. 27, 1987	Heilveil et al.	365	559	
	4,468,733	Aug. 28, 1984	Oka et al.	710	312	
	4,426,685	Jan. 17, 1984	Lorentzen	713	401	
	4,408,272	Oct. 4, 1983	Walters	710	61	
	4,257,097	Mar. 17, 1981	Moran	711	119	
	3,846,763	Nov. 5, 1974	Riikonen	710	70	
$T N T$	H696	Oct. 3, 1989	Davidson	364	900	

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EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION YES/NO	

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

EXAMINER TAN T. NGUYEN	DATE CONSIDERED 08/07/03

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TNT	5,847,997	Dec. 8, 1998	Harada et al.	365	185. 33	
	5,148,523	Sep. 15, 1992	Harlin et al.	345	579	
	5,142,637	Aug. 25, 1992	Harlin et al.	711	106	
	5,109,498	Apr. 28, 1992	Kamiya et al.	711	123	
	4,954,987	Sep. 4, 1990	Auvinen et al.	365	189.02	
TNT	4,937,734	Jun 26, 1990	Bechtolsheim	711	202	

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TNT	61028248	Feb. 7, 1986	Japan				

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TNT ✓	S. Muchmore, "Designing Computer Systems Based on Multibus II", New Electronics, Vol. 20, No. 16, p. 31-32, Aug. 11, 1987.
TNT ✓	"Accordion Start-Stop Sequencer for a Variable Cycle Storage Controller", IBM Technical Disclosure Bulletin, pp. 2074-2075, (a delphion.com reprint on two sheets), Oct. 1986.
TNT ✓	"Motorola 68030 Cache Organization", posting to Internet Newsgroup net.arch by aglew@ccvaxa.UUCP, (a google.com reprint on two sheets), Sep. 29, 1986.

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TNT	4,920,483	Apr. 24, 1990	Pogue et al.	711	219	
	4,912,630	Mar. 27, 1990	Cochcroft Jr.	711	211	
	4,807,189	Feb. 21, 1989	Pinkham et al.	365	189.05	
	4,799,199	Jan. 17, 1989	Scales, III et al.	365	230.08	
	4,788,667	Nov. 29, 1988	Nakano et al.	365	193	
	4,680,738	Jul. 14, 1987	Tam	365	239	
	4,675,850	Jun. 23, 1987	Kumanoya et al.	365	230.01	
	4,519,034	May 21, 1985	Smith et al.	710	61	
	4,315,308	Feb. 9, 1982	Jackson	710	33	
	4,247,817	Jan. 27, 1981	Heller	327	63	
	4,092,665	May 30, 1978	Saran	341	63	
	4,084,154	Apr. 11, 1978	Panigraphi	365	222	
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TNT	M. Bazes et al., "A Programmable NMOS DRAM Controller for Microcomputer Systems with Dual-Port Memory and Error Checking And Correction", IEEE Journal of Solid State Circuits, Vol. 18, No. 2, pp. 164-172, Apr. 1983.
TNT	R.W. Callahan et al., "Burst-Mode PIO Bus Control", IBM Technical Disclosure Bulletin, Vol. 21, No. 4, pp. 1417-1419, (a delphion.com reprint on two sheets), Sep. 1978.

EXAMINER Tan T. Nguyen	DATE CONSIDERED 08/07/03

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TNT	120553/1987	Jun. 1, 1987	Japan				
/	0 189 576	Aug. 6, 1986	EPO				
	0 187 289	Jul. 16, 1986	EPO				
	0 166 192	Jan. 2, 1986	EPO				
	0 126 976	Dec. 5, 1984	EPO				
	135684/1984	Aug. 3, 1984	Japan				
	82/02615	Aug. 5, 1982	PCT				
TNT	33471/1982	Feb. 23, 1982	Japan				

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TNT	Stanley D. Rosenbaum et al., "A 16 384-Bit High-Density CCD Memory", IEEE Journal of Solid State Circuits, Vol. SC-11, No. 1, pp. 33-39, Feb. 1976.
TNT	Hopper et al., "Multiple vs. Wide Shared Bus Multiprocessors", Proceedings of the 16th Annual International Symposium on Computer Architecture, Jerusalem, Israel, June 1989, IEEE Computer Society Press, 1989.
TNT	Gerninger et al., "A Survey of Commercial Parallel Processors", Computer Architecture News, Vol. 16, no. 4, Sept. 1988

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TNÝ	5,598,554	Jan. 28, 1997	Litaize et al.	7/6	1	
	4,644,469	Feb. 17, 1987	Sumi	7//	2//	
	4,747,081	May 24, 1988	Heilveil et al.	365	219	
	6,112,287	Aug. 29, 2000	Litaize et al.	7/2	//	
TNT	4,782,439	Nov. 1, 1988	Borkar et al.	7/0	23	

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TNT	Dubois et al., "Effects of Cache Coherency in Mulitprocessors", IEEE Transaction in Computers, Vol. C31, No. 11, pgs. 1083-1099, November 1982.

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